Amiga

A1200_R2

Rev.1.38 (06.09.2012)
**Signal Glossary**

**Jumpers and Stuff**

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<td>R625</td>
<td>NMS.CP00 Buss 1</td>
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<td>NMS.CP00 Power Buss</td>
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**Connectors**

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**Key Components**

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**Description (Area)**

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<tr>
<th>DESCRIPTION</th>
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<td>PAGE</td>
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<td>RCS-200</td>
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<td>28MHz SIGNAL</td>
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<td>Left Right Audio (Audio)</td>
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<td>Interrupt Priority Level (68000)</td>
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<td>Interrupt Request (Chips)</td>
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<td>Index Pulse (Floppy)</td>
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**Signal Glossary**

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<tr>
<td>HSYNC</td>
<td>Horizontal Sync (Video)</td>
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<td>VSYNC</td>
<td>Vertical Sync (Video)</td>
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<td>Transmit Data (RS232 Port)</td>
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<td>TRK0</td>
<td>Track Zero Sense (Floppy)</td>
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<td>STEP</td>
<td>Step Direction (Floppy)</td>
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<td>VPA</td>
<td>Vertical Position Adjust (Video)</td>
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<td>VMA</td>
<td>Vertical Magnification Adjust (Video)</td>
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<td>Row Address Strobe (DRAM)</td>
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<td>CCK/CCKQ</td>
<td>Color Clock / Quadrature (Chips)</td>
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<td>Device Busy (Parallel Port)</td>
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<td>OVL</td>
<td>Overline (Video)</td>
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<td>Mouse 0 Quadrature V/H (Joysticks)</td>
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if rom16="yes"

OPTIONAL REAL-TIME CLOCK/CALENDAR

A1200 Rev 2 PCB

if rom32="yes"

OPTIONAL FLASH MEMORY

if flash="yes"

BUGGERY

SHORT PCB TRIX

This section define an additional DB9/ header pair on a breakaway part of the so that a shorter PCB can be used.

16 AND 32-BIT SOCKETS MAY OVERLAP!

16-BIT ROM

32-BIT ROM

if flash="yes"

if short="yes"

13) Generate fast ROM OE
2) Inhibit _DSACK for INTACK
3) Generate fast ROM OE
4) Optional ROM Wait State
5) Gate _CE_ENA with _AS
to be fixed by Gayle rev and U37
Warning: Byte Swapped

ONLY PINS 19-40 LOADED FOR 2 M-BYTE CONFIGURATION

IDE DRIVE

A1200 Rev 2 PCB
A1200 Rev 2 PCB
A1200 LED PCB ASSEMBLY REV1

- LED1: POWER GREEN
- LED2: FLOPPY YELLOW
- LED3: HDD YELLOW
- R1: 33
- R2: 33
- R3: 33

4-PIN SIL CN1

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