Amiga

A500_R6

Rev.1.37 (02.09.2012)
Note: Various components are for EMI Control and may be loaded with funny things...
JP2 controls where expansion ram maps to:
A23 -> C00000 (default), A19 -> B80000
XR1 is added to some boards per ECD 880283

Rock Lobster
04/27/89
A500 Rev 6a/7 PCB
Note: RP105-RP111 are optional internal bus termination, and are not normally loaded.

Note: JP3 swaps internal vs. expansion ram
Note: LED off, Filters bypassed

Note: Ground interconnection near audio jacks.
Note: RP405 Not Used

Note: Components designated as Exxx may be loaded with EMI filters, ferrite beads or resistors!
NOTE: HEAVY LINES INDICATE A SINGLE POINT CONNECTION

POWER INPUT

FLOPPY POWER

Note: Some drives are +5 only...

SPARES

GROUNDED HOLES, &c.

DECOUPLING
Notes: Some configurations of this RAM expansion require mods to rev 3 and 5 A500 boards and/or use of the Agnus HR 2MB bond-out.
U1-U4 are generic 256K-bit x 4 128 nS DRAM 
RP911,RP912 are optional DRD Termination 
C10 is optional A8/RAS Setup Time Control 
TP9 is Clock Calendar Frequency Test Point

Real Time Clock

U9
M568242B

TP9

Real Time Power

Decoupling...